EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	49	CAM same RAM same main near3 memory and ("712"/\$.ccls. "718"/\$.ccls. "711"/\$.ccls.)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 17:58
L7	12	CAM same RAM same main near3 memory same (I/O input output) and ("712"/\$.ccls. "718"/\$.ccls. "711"/\$.ccls.)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 17:59
L8	8	CAM same RAM same main near3 memory same instruction and ("712"/\$.ccls. "718"/\$.ccls. "711"/\$.ccls.)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 18:03
L9	6	8 not 7	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 18:03
S132	169	reus\$4 with instruction same storage same execut\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:19
S133	9	reus\$4 with instruction with table same storage same execut\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:19
S134	4	reus\$4 with instruction same storage same execut\$4 same pattern	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:19
S135	8	S133 not S134	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:21
S136	53		US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:22
S137	28	S136 and ("718"/\$.ccls. "712"/\$.ccls.)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:22
S138	21	S137 not (S133 S134)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:23
S139	24	S136 not (S133 S134 S137)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/08 10:25

S140	2	712/E9.029.ccls. and @pd> "20100213"	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 10:56
S141	40	712/210.ccls. and @pd>"20100209"	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 10:56
S142	28	712/209.ccls. and @pd>"20100209"	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 10:57
S143	151	712/225.ccls. and @pd>"20100209"	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 10:57
S145	21	(S140 S141 S142 S143) and (instruction same reus\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 10:58
S146	46	(S140 S141 S142) and (instruction same (reus\$4 depend\$5))	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:01
S147	37	S146 not S145	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:02
S148	0	(instruction same reus\$4 same depend\$5 same matrix same (CAM RAM))	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:03
S149	5	(instruction same reus\$4 same depend\$5 same matrix)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:03
S150	180	(instruction with reus\$4 same depend\$5)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:04
S151	6	(instruction with reus\$4 same depend\$5 with relation\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:04
S152	6	S151 not S149	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:04
S153	0	(instruction with reus\$4 same depend\$5 same matrix)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:06
S154	9	(instruction with reus\$4 same depend\$5 same (CAM RAM window))	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:06
S155	8	S154 not (S151 S149)	US-PGPUB; USPAT; USOCR;	OR	ON	2011/06/22 11:06

			IBM_TDB		***************************************	***************************************
S156	96	(instruction with reus\$4 same dependency)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:09
S158	5	(instruction with reus\$4 same dependency same relation\$4)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:09
S159	2	S158 not (S154 S151 S149)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:09
S160	30	(instruction near3 reus\$4 same dependency)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:10
S161	27	S160 not (S158 S154 S151 S149)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:10
S162	89	(instruction near3 reus\$4 same depend\$5)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:18
S163	59	S162 not (S160 S158 S154 S151 S149)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:18
S164	6	(CAM with RAM) with reus\$4	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	ON	2011/06/22 11:34
S165	11	CAM same RAM same main near3 memory same input same output	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 15:46
S166	0	CAM same RAM same main near3 memory same input same output same pattern	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 15:46
S167	0	CAM same RAM same main near3 memory same pattern	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 15:47
S168	71	CAM same RAM same pattern same input same output	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 15:50
S169	6	CAM same RAM same pattern same input same output same processor	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 15:51
S170	6	S169 not S165	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	OFF	2011/06/22 15:51
S171	65	S168 not (S165 S169)	US-PGPUB;	OR	OFF	2011/06/22

		USPAT; USOCR; IBM_TDB			15:51
 S172	S171 and ("718"/\$.ccls. "712"/\$.ccls. "711"/\$.ccls.)	US-PGPUB; USPAT; USOCR; IBM_TDB	OR	,	2011/06/22 15:52

6/ 22/ 2011 6:21:18 PM

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